



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE COMMISSIONER OF PATENTS

Applicant : 10/643,909
Applicant : Elizabeth Foster et al
Filed : 08/20/2003
Art Unit : 1733
Examiner : John T. Haran
Docket No. : EI-2-03-009
Title : Circuitized Substrate and Method of Making Same
Assignee : Endicott Interconnect Technologies, Inc.

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

RESPONSE AFTER FINAL REJECTION

This Paper is in response to the Office Action dated October 17, 2005, made final by the Examiner. Entry is respectfully requested to allow Applicants to properly respond to the new arguments raised by the Examiner in the final Office Action and to present Claims and arguments which overcome this rejection, thereby expediting prosecution of this Application. Applicants respectfully submit that the Claims now presented are patentable over the documents of record herein and are thus allowable. Should the Examiner disagree, however, entry of this response is respectfully urged to place the Claims in better condition for consideration upon appeal.

There are no Amendments to the Specification because the initial and final Office Actions do not indicate a need for same. (Applicants have once again reviewed the status of the two co-pending Applications cited on page 2 of the Specification and neither of these have issued as of the date of filing this Response.)